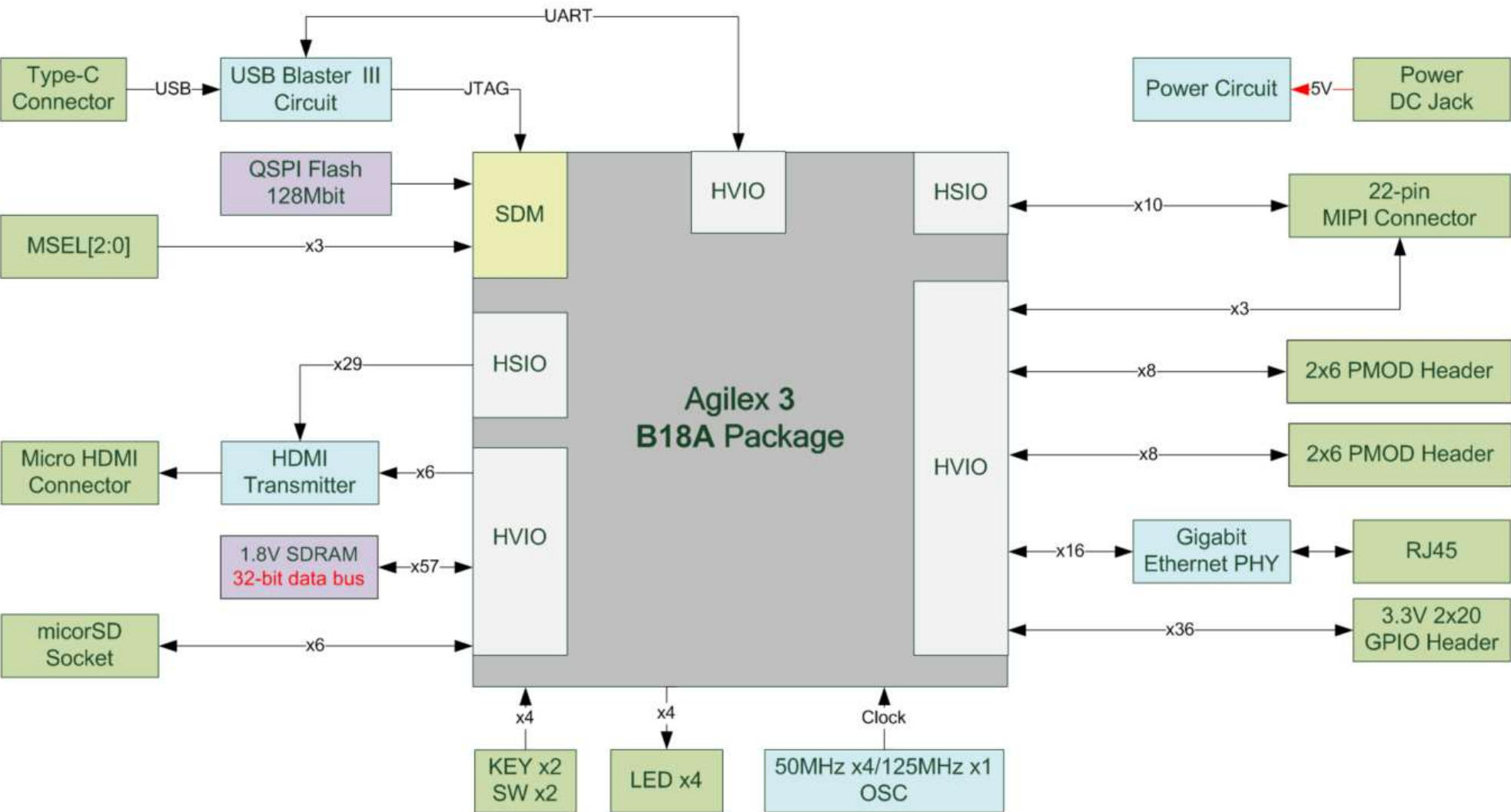
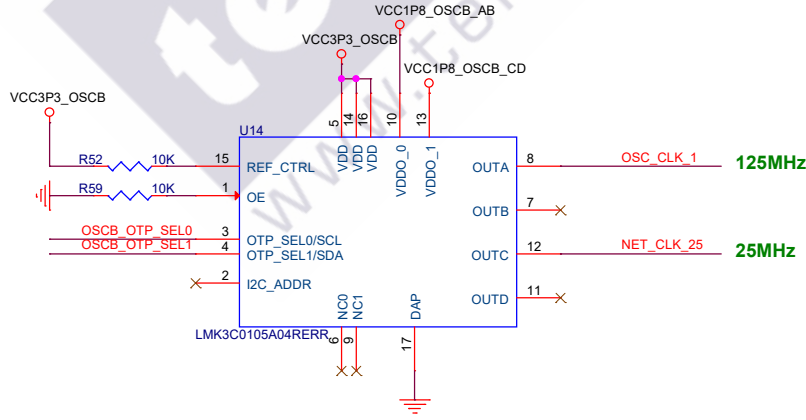
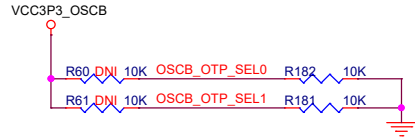
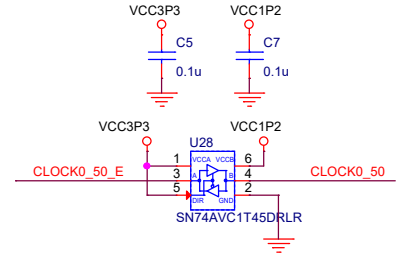
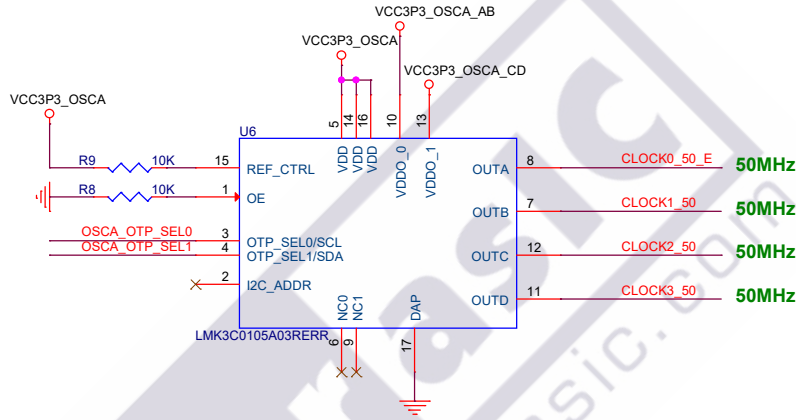
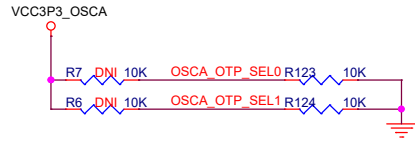
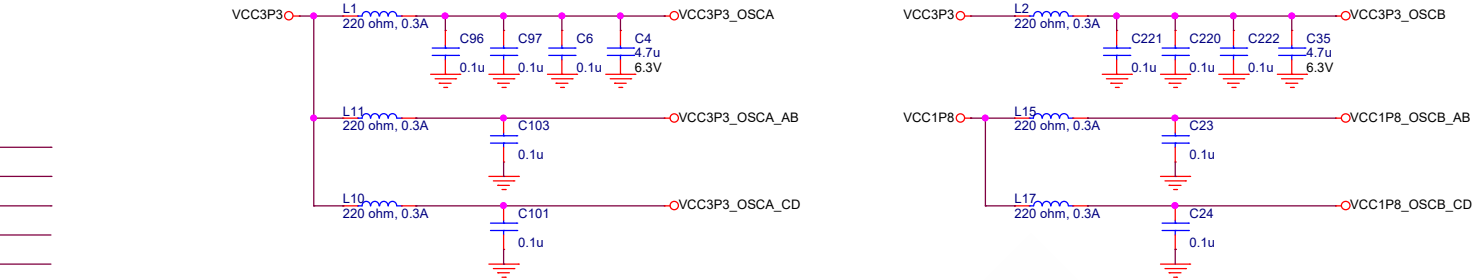
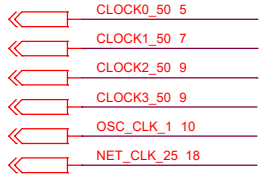


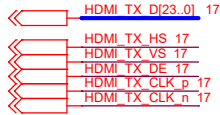
Block Diagram



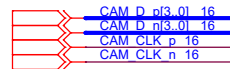
Clock Interface



HDMI TX Interface



MIPI Camera Interface



User IO Interface

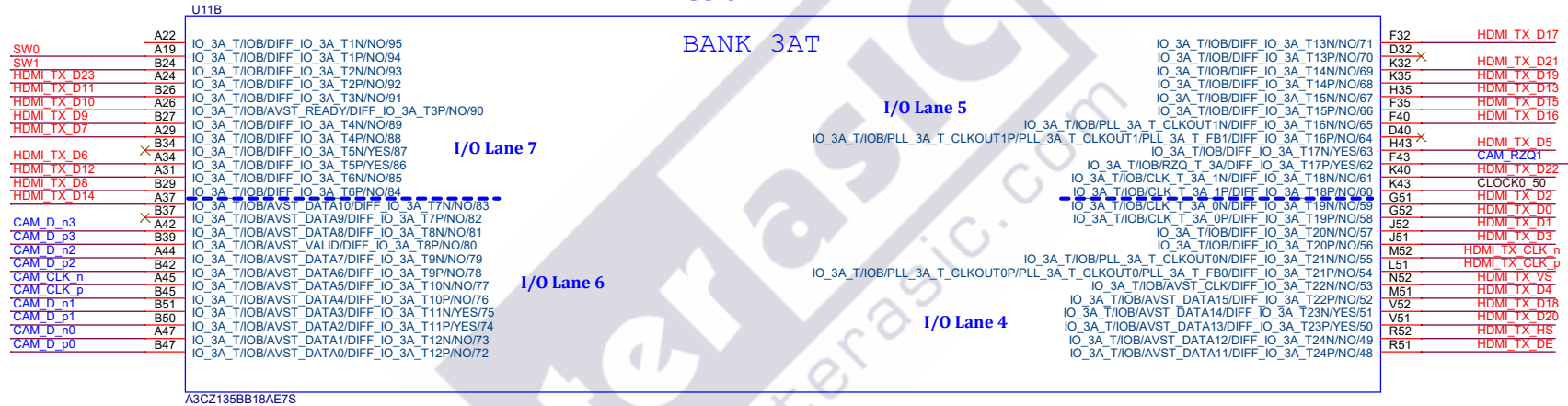


Clock Interface

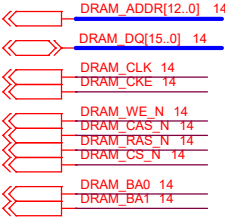


VCCIO = 1.2V

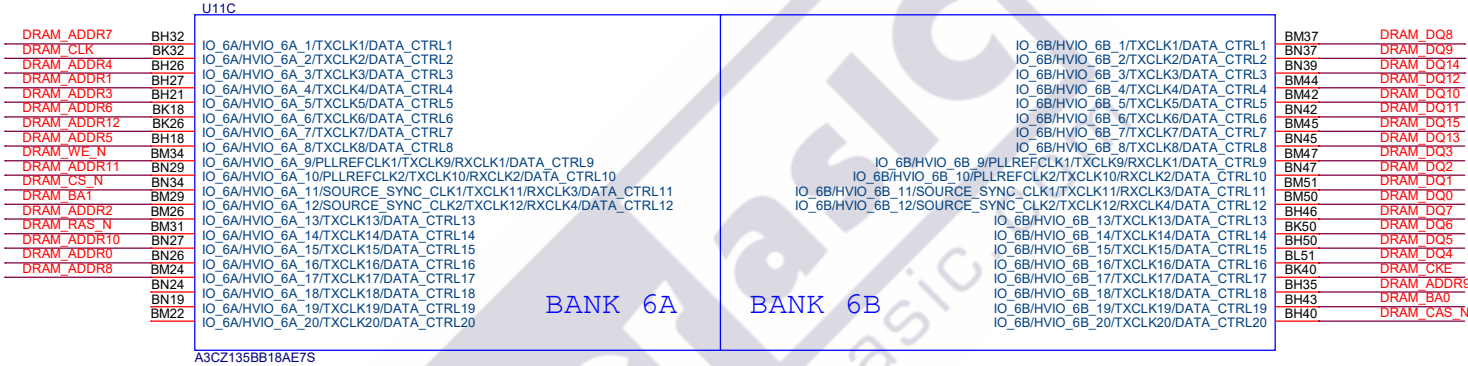
BANK 3AT



SDRAM Interface



VCCIO = 1.8V



User IO Interface

GPIO_D[35..0] 15

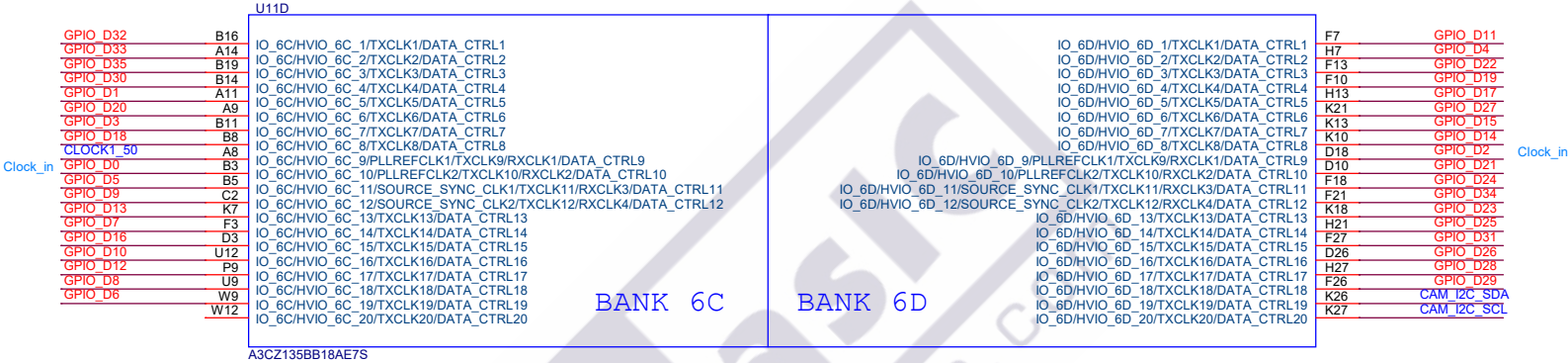
MIPI Camera Interface

CAM_I2C_SDA 16
CAM_I2C_SCL 16

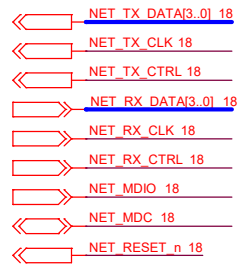
Clock Interface

CLOCK1_50 3

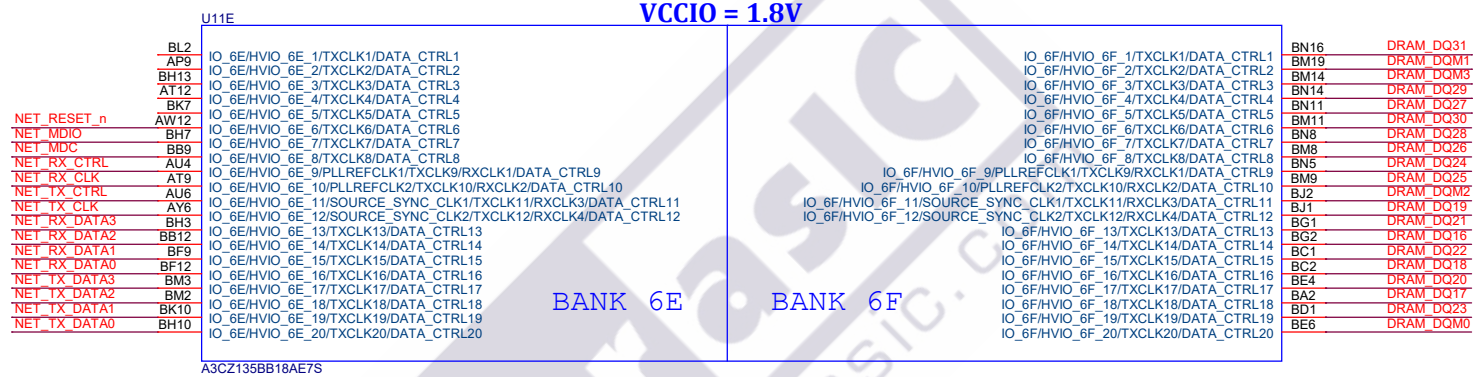
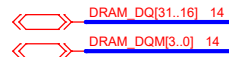
VCCIO = 3.3V



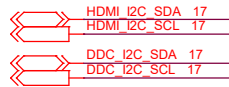
Ethernet Interface



SDRAM Interface



HDMI I2C Interface



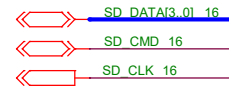
User IO Interface



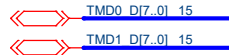
MIPI Camera Interface



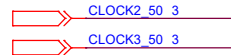
SD Card Interface



TMD Interface



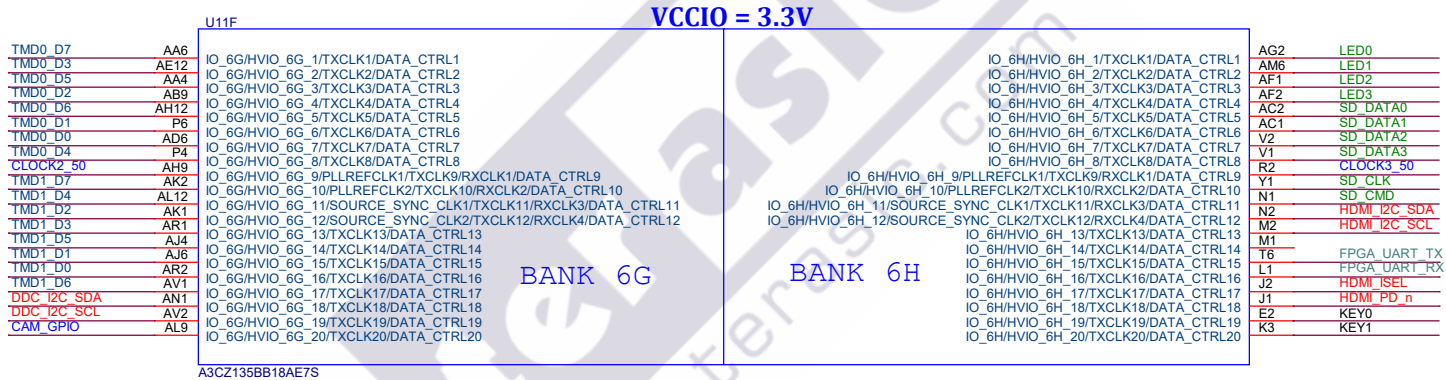
Clock Interface



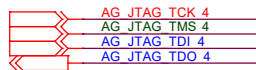
FPGA UART Interface



HDMI Ctl Interface

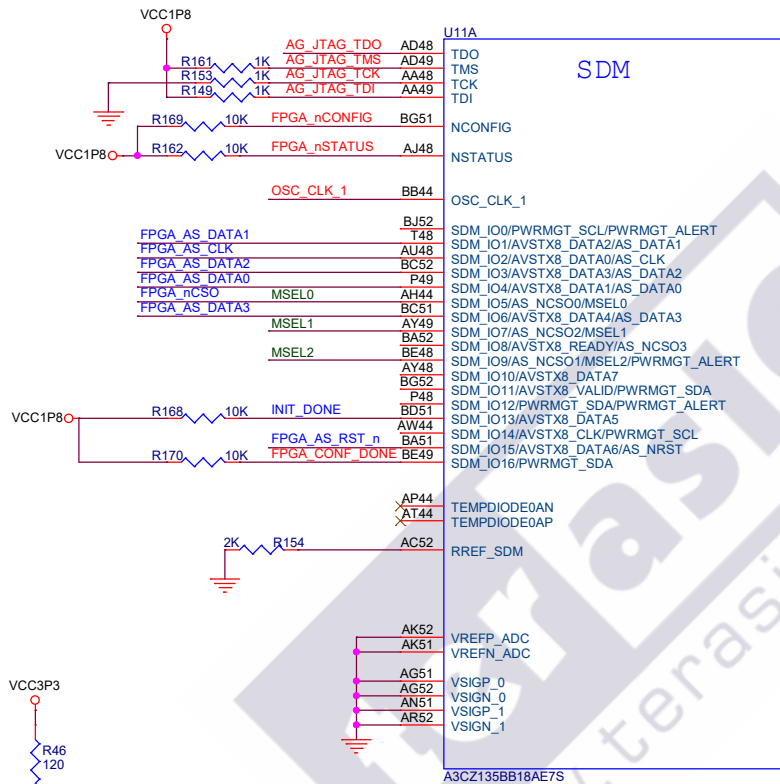
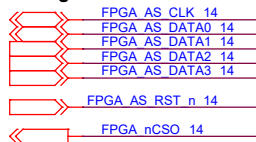


Agilex JTAG Interface

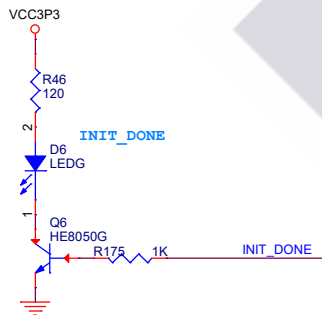
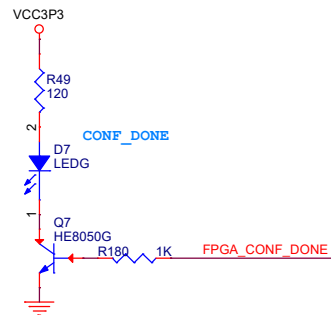
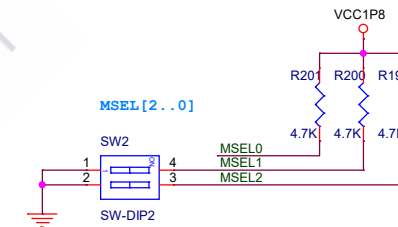


OSC_CLK 1 3

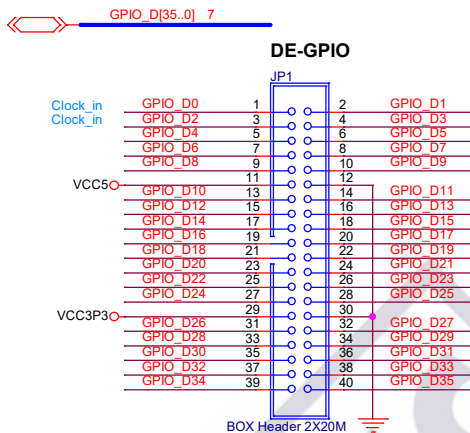
FPGA AS Configuration



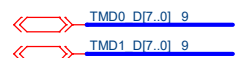
| MSEL[2:0] | Configuration Mode |
|-----------|-----------------------------|
| 001 | AS - Fast (Default Setting) |
| 111 | JTAG |



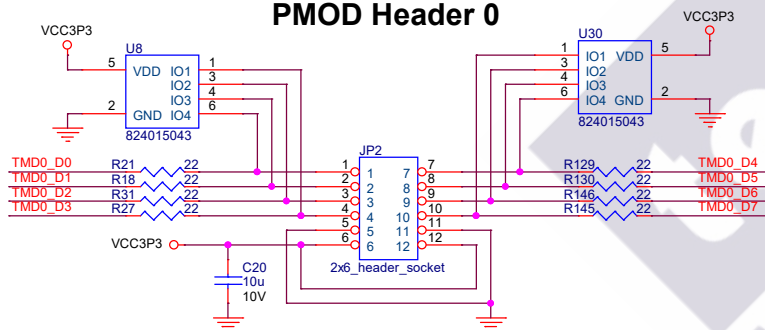
GPIO



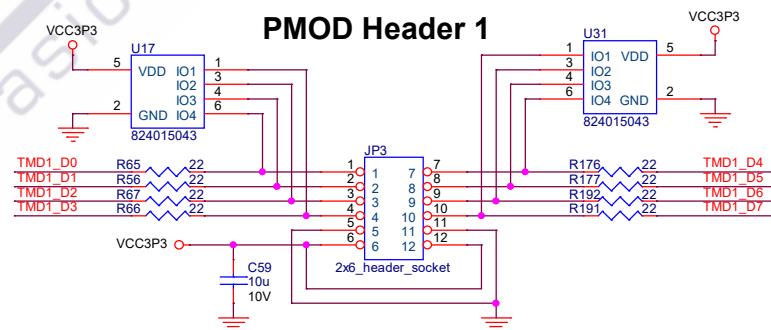
TMD Interface



PMOD Header 0

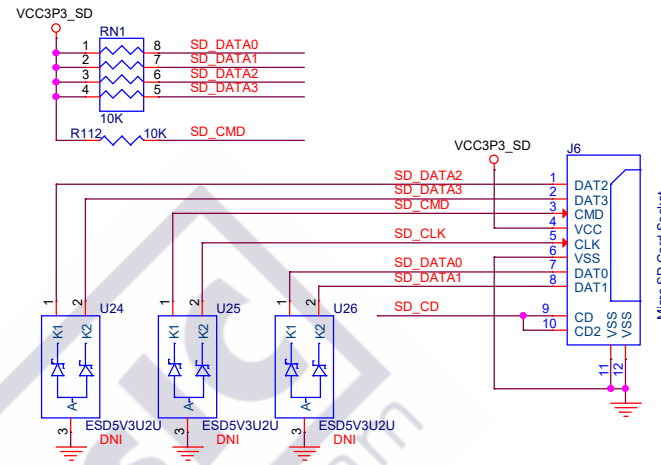
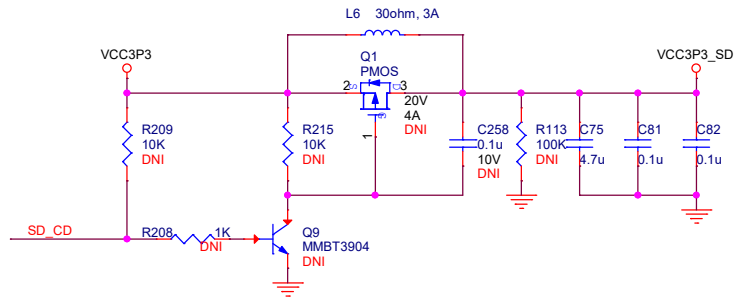


PMOD Header 1



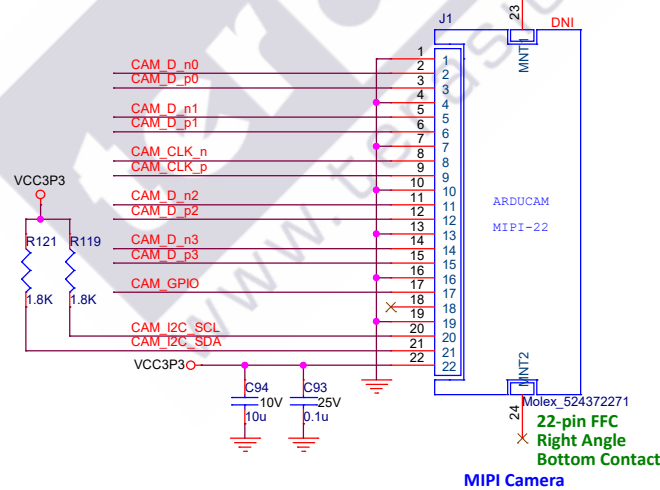
Micro SD Card

SD_DATA[3..0]_9
SD_CMD_9
SD_CLK_9



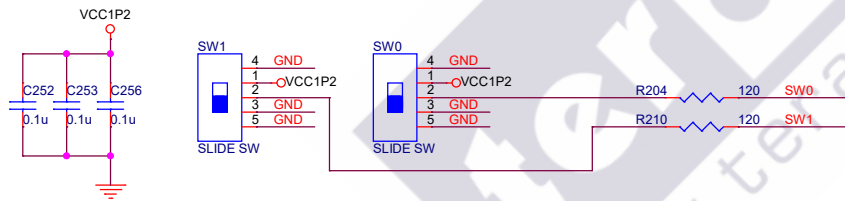
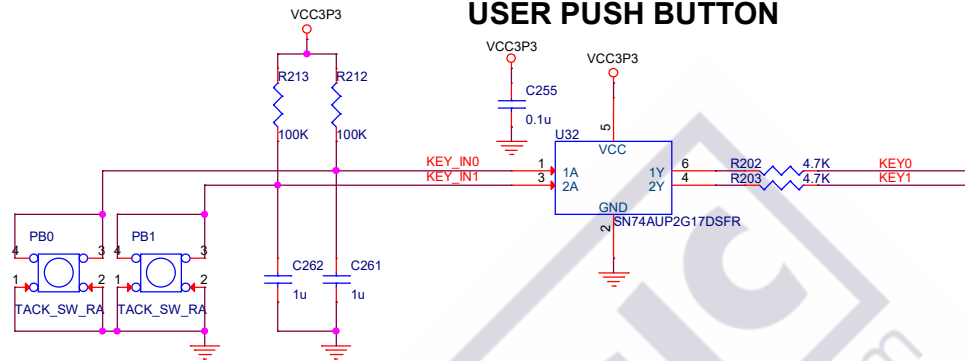
MIPI Camera Interface

CAM_D_p[3..0]_5
CAM_D_n[3..0]_5
CAM_CLK_p_5
CAM_CLK_n_5
CAM_GPIO_9
CAM_I2C_SDA_7
CAM_I2C_SCL_7



Note: Connector DNI for Z code FPGA

KEY1.01_9
SW1.01_5
LED3.01_9



USER LEDS

